

<b>Notice of References Cited</b>	Application/Control No. 09/434,736		Applicant(s)/Patent Under Reexamination KIM ET AL.	
	Examiner Evan T. Pert		Art Unit 2813	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification	
X	A	US-5,318,923	06-1994	Park	--	--
X	B	US-5,069,749	12-1991	Gutierrez	--	--
	C	US-4,707,457	11-1987	Erb	--	--
X	D	US-5,683,938	11-1997	Kim et al.	--	--
	E	US-				
	F	US-				
	G	US-				
	H	US-				
	I	US-				
	J	US-				
	K	US-				
	L	US-				
	M	US-				

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification	
	N						
	O						
	P						
	Q						
	R						
	S						
	T						

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, SILICON PROCESSING FOR THE VLSI ERA - VOLUME II, 1990, Lattice Press, page 192.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.